

• ABOUT MSBTE

MSBTE is an autonomous Board of Technical Education in the state of Maharashtra, India. It designs and implements Diploma, Post Diploma and advanced Diploma Programs to affiliated Institutions. The board was established in 1963 to cater the increasing needs of affiliated institutions and their students.

Maharashtra State Board of Technical Education was established to ensure quality Technical Education at Diploma level to match the latest trends in the industry. This leads to all round personal development of a student socially and globally.

It always organizes, promotes and encourages various programs for students like technical paper presentation, project competitions, career fairs etc.

• ABOUT THE INSTITUTE



Government Polytechnic, Washim was established in the year 1982. The institute is located on Risod road Washim having 52 acres of land. The Institute is running six branches. The Institute has good infrastructure facilities and laboratories fully equipped with equipments. The institute is also running Community development through polytechnic (CDTP) scheme funded by the Central Government of India.

• Location

Washim is one of the district places in Maharashtra and it is situated on South Central Railway. Washim is known for historical temple of Lord Balaji having rich heritage.

• Transportation Facilities

Auto rickshaws are available from bus stand and railway station to the Institute.

- Distance from Bus Stand – 3 km
- Distance from Railway Station – 4 km

• Accommodation Facilities

Accommodation facility will be provided in the Institute hostel. The students who require accommodation before the day of the event or after the event should intimate the co-ordinator well in advance.

THEME OF PAPER PRESENTATION

For Electronics Engineering Group

Papers related on following topics

- Raspberry PI applications/Arduino applications
- Wireless sensor networks
- Neural Network/ Fuzzy logic
- Li-fi Technology
- Electronic System Design & Manufacture (ESDM)
- Advancement in medical Instrumentation.

Rules and Regulations for Paper Presentation

1. Only final year students of the Institute affiliated to MSBTE & Autonomous Polytechnics are eligible to participate in the event.
2. The maximum number of authors will be four for same Institution.
3. Only one entry from each institute will be finalized.
4. The subject/topics of Paper must be strictly technical & related to the Discipline only. Length of Paper should be between 1800 to 2000 words (Approx. 180 to 200 Lines or 4 to 5 pages).
5. The student authors will deliver a seminar on their topic for a maximum of 10 minutes.
6. One hard copy and one soft copy of the documents should be submitted.
7. One soft copy of full length (camera ready) technical paper (in msword 2010) and registration form duly signed by the Principal should be emailed at: ejdeptgpw@gmail.com or luckylonkar@gmail.com on or before **08/09/2017**.
8. Soft copy of proceedings of the event will be made available on

Website: www.ejgpwashim.wordpress.com

Registration Form MSBTE STATE LEVEL TECHNICAL PAPER PRESENTATION COMPETITION (ELECTRONICS ENGG. GROUP) NAGPUR REGION ON 14TH SEPTEMBER, 2017

Name of the Institute: _____

Institute Code: ____ Branch / Course : _____

Date of Competition: 14/09/2017

Title of the paper: _____

Names of the Presenters:

1. _____

2. _____

Email id of Presenter: _____

Contact Number: _____

Email id of Co. Presenter: _____

Contact Number: _____

Name of Incharge Staff Member: _____

Whether accommodation required: Yes/No.

No. of Boys: __ No. of Girls: __

Recommendation of Head of Institute

I have recommended above students to participate in MSBTE State Level Technical Paper Presentation Competition 2017-2018 at your institute. I also certify that the said students are bonafide students of Final Year Diploma of this Institute.

Sign. of the Participant/s

Sign. of Principal

1. _____

2. _____

Date: /09/ 2017

Seal of Institute

Note: This application form after duly filling should be scanned and emailed to concerned co-ordinator of the host institute conducting the competition followed by telephonic confirmation with him/her. Original form should be brought at the time of the event.

• **IMPORTANT DATES**

Last date of receipt of papers by mail : - 08/09/2017
Confirmation of paper by mail : - 11/09/2017
Paper presentation competition : - 14/09/2017

• **DATE & REPORTING TIME OF EVENT**

Date: - 14/09/2017

Reporting time: - 9.30 A.M.

• **Guide Lines for Paper Submission**

• **Paper Format**

Softcopy in msword 2010

Paper Size : A4, 70 gsm, White

Font: Times New Roman

Font Size: Title: 14 Bold

Sub Title: 12 Bold

Text: 12 Normal + 12 pt.

Margins: Top 0.75", Bottom -1"

Left: 0.625", Right: 0.625"

Gutter - 0", Gutter Position - Left

Line Spacing: 1.5

Page Numbering on Right Bottom of each page.

Printing only in TWO column & one side of paper.

Column - 3.5 inch wide

0.25 - inch gap between two columns

• **Content Format**

Title of Page

Names of Authors (Max. 04)(2 Students+Lecturer+HOD)

Name of Institute, Abstract, Keywords, Contents,

Conclusion, References.

• **Note**

Tea and Lunch for the Participants will be included in the event.

Travelling allowance will be paid to participants as per MSBTE norms. Travelling expenses limited to ST bus fare (Excluding Asiad or Luxury buses) or Railway Second Class Sleeper fare will be paid to the students by nearest route on submission of tickets.

• **Prizes** First Prize : Rs. 7500/-

Second Prize : Rs. 5000/-

Consolation Prize : Rs. 2500/-

Certificates to all participants

• **PATRONS** •

Shri. C. V. Oak (IAS),

(Hon. Director, DTE, Maharashtra State, Mumbai)

Dr. Abhay Wagh

(Hon. Director, MSBTE, Mumbai)

Dr. D. N. Shingade

(I/C Joint Director DTE, Amravati Region)

Dr. V. M. Mohitkar

(Secretary, MSBTE, Mumbai)

• **ADVISORY COMMITTEE** •

Dr. D. K. Gupta

(I/c Principal, G.P. Washim)

Dr. S. J. Patil

(Dy. Secretary MSBTE, Nagpur Region)

• **CHIEF COORDINATOR** •

Dr. V. J. Dongre

(HOD Electronics & Telecom. Mb. 9370668979)

• **PROGRAM COORDINATOR** •

Shri. L. K. Lonkar

(Lecturer in Electronics & Telecom Mb. 9822976562)

• **CO-COORDINATORS** •

Shri. A. G. Raut (Lecturer ETX, Mob. 7588041811)

Shri. P. M. Bhuyar (Lecturer ETX, Mob. 9422856159)

Shri. R. R. Jaiswal (Lecturer ETX, Mob. 9764996880)

Smt. S. S. Jayade (Lecturer ETX, Mob. 8007521521)

• **CO-ORDINATION COMMITTEE** •

Dr. A. M. Jinturkar (HOD Mechanical Engg. Dept.)

Dr. S. B. Kulkarni (HOD Electrical Engg. Dept.)

Smt. M. R. Lolure (I/c HOD Information Technology Dept)

Shri. D. K. Bawane (I/c HOD Automobile Engg. Dept)

Shri. N. S. Vidyasagar (Lect. Civil Engg. Dept.)

Shri. K. P. Joshi (Workshop Superintendent)

Shri. U. A. Nage (I/c Science Dept.)

**MAHARASHTRA STATE BOARD OF
TECHNICAL EDUCATION, MUMBAI**

(AUTONOMOUS)(ISO 9001-2008) (ISO/IEC27001-2005)



**MSBTE STATE LEVEL TECHNICAL PAPER
PRESENTATION COMPETITION**

ELECTRONICS ENGINEERING GROUP

NAGPUR REGION

ON

14TH SEPTEMBER, 2017



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Telecommunication Engineering

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